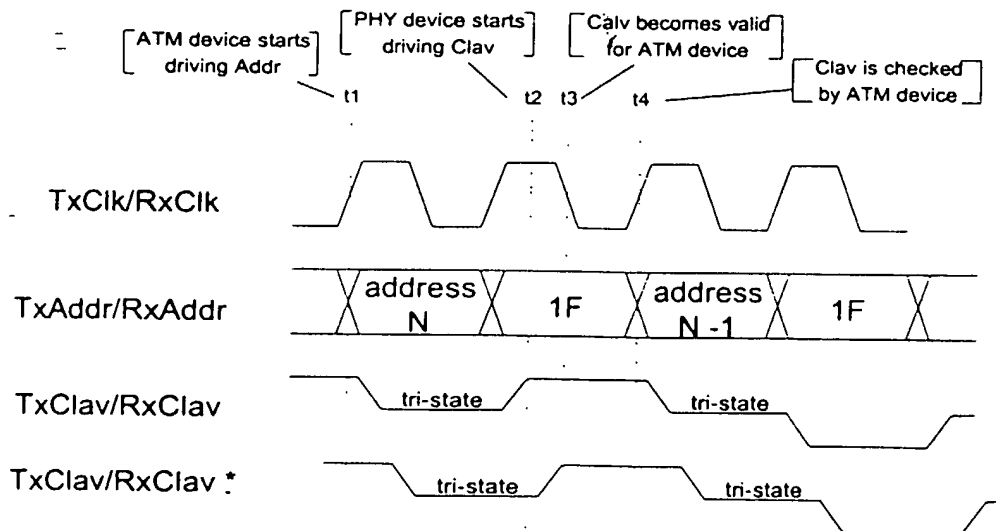


FIGURE 1. An Example of UTOPIA interface

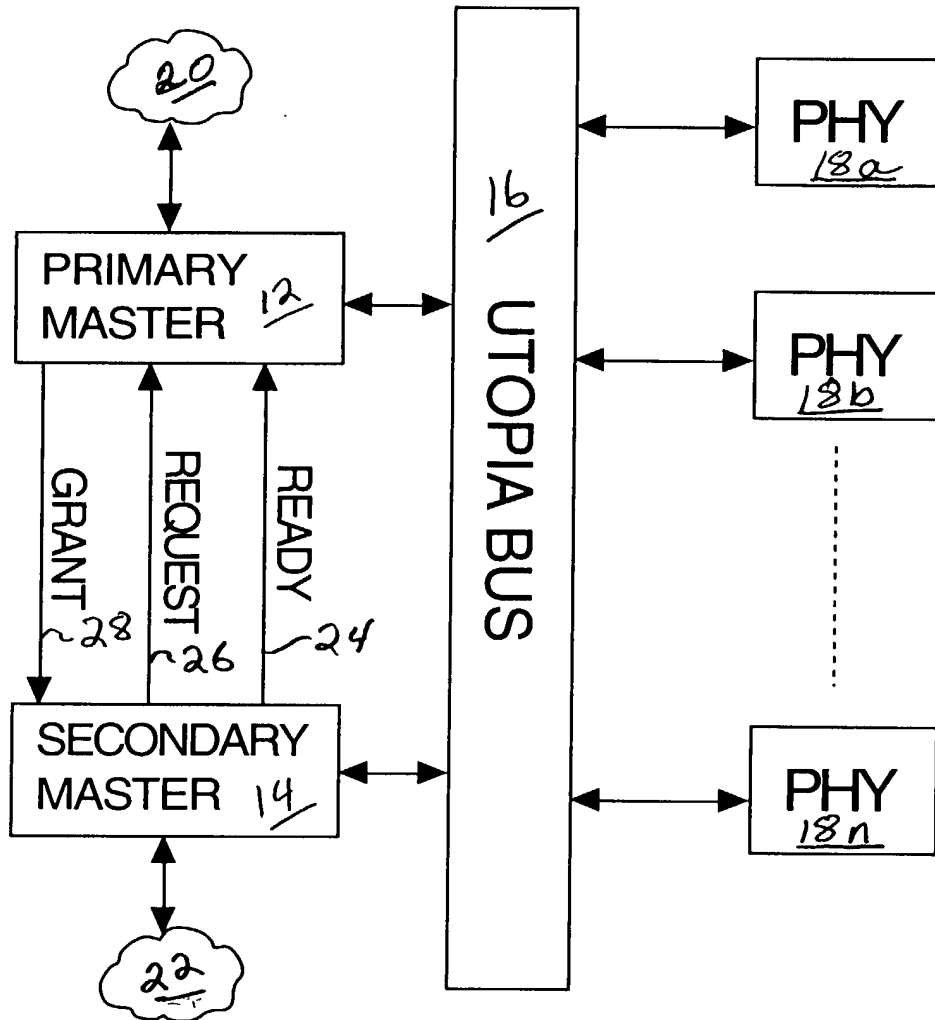
PRIOR ART
FIG. 1



Note: TxClav/RxClav * are the Clav signals after transmission delay from PHY device to ATM device

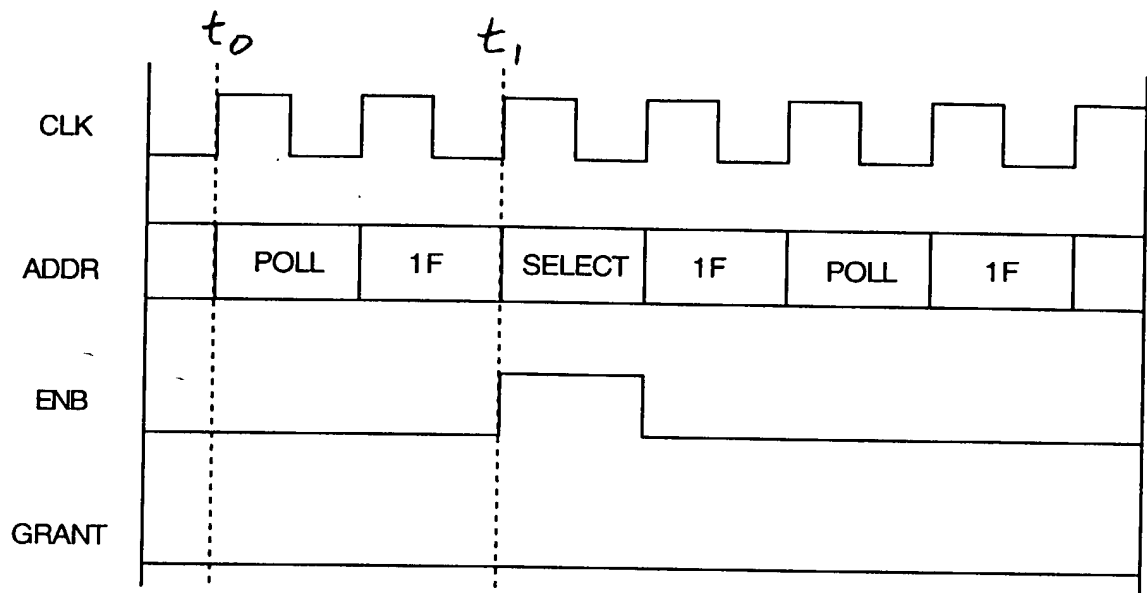
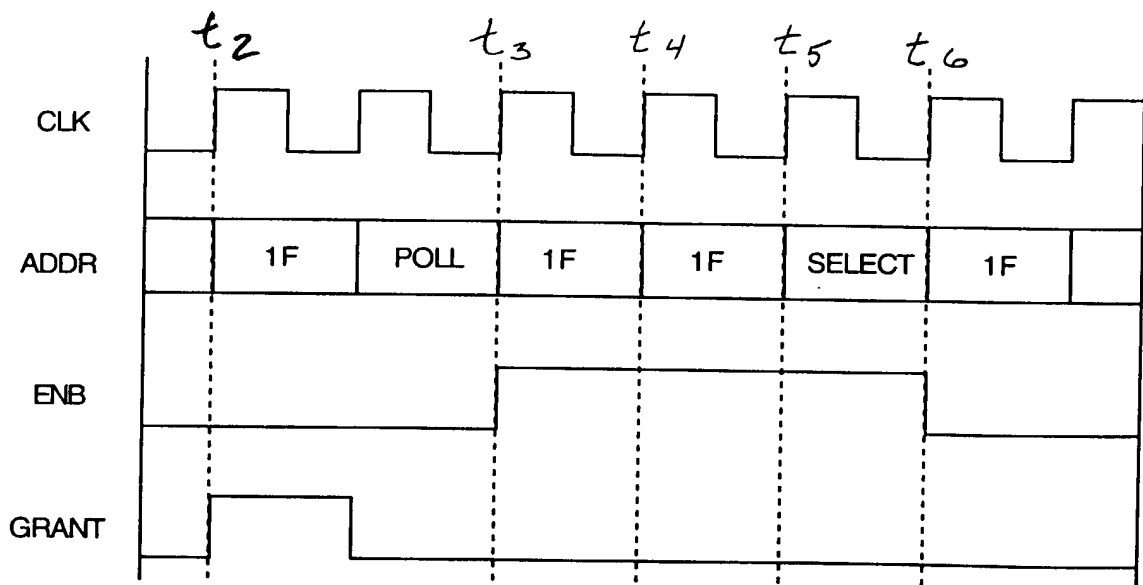
FIGURE 2. UTOPIA level 2 address polling timing diagram

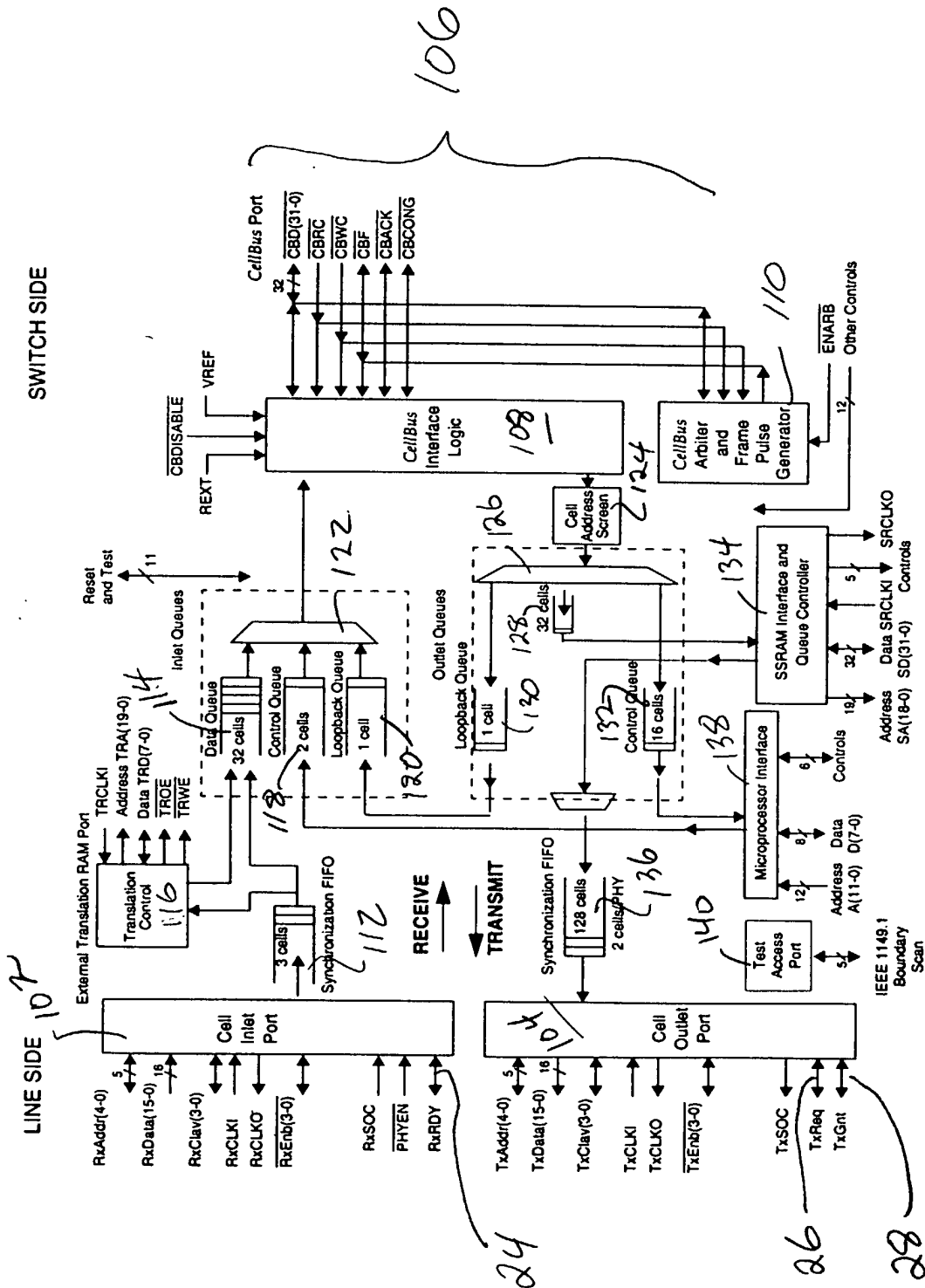
PRIOR ART
FIG. 2



10 ↗

Figure 3

**Figure 4a****Figure 4b**



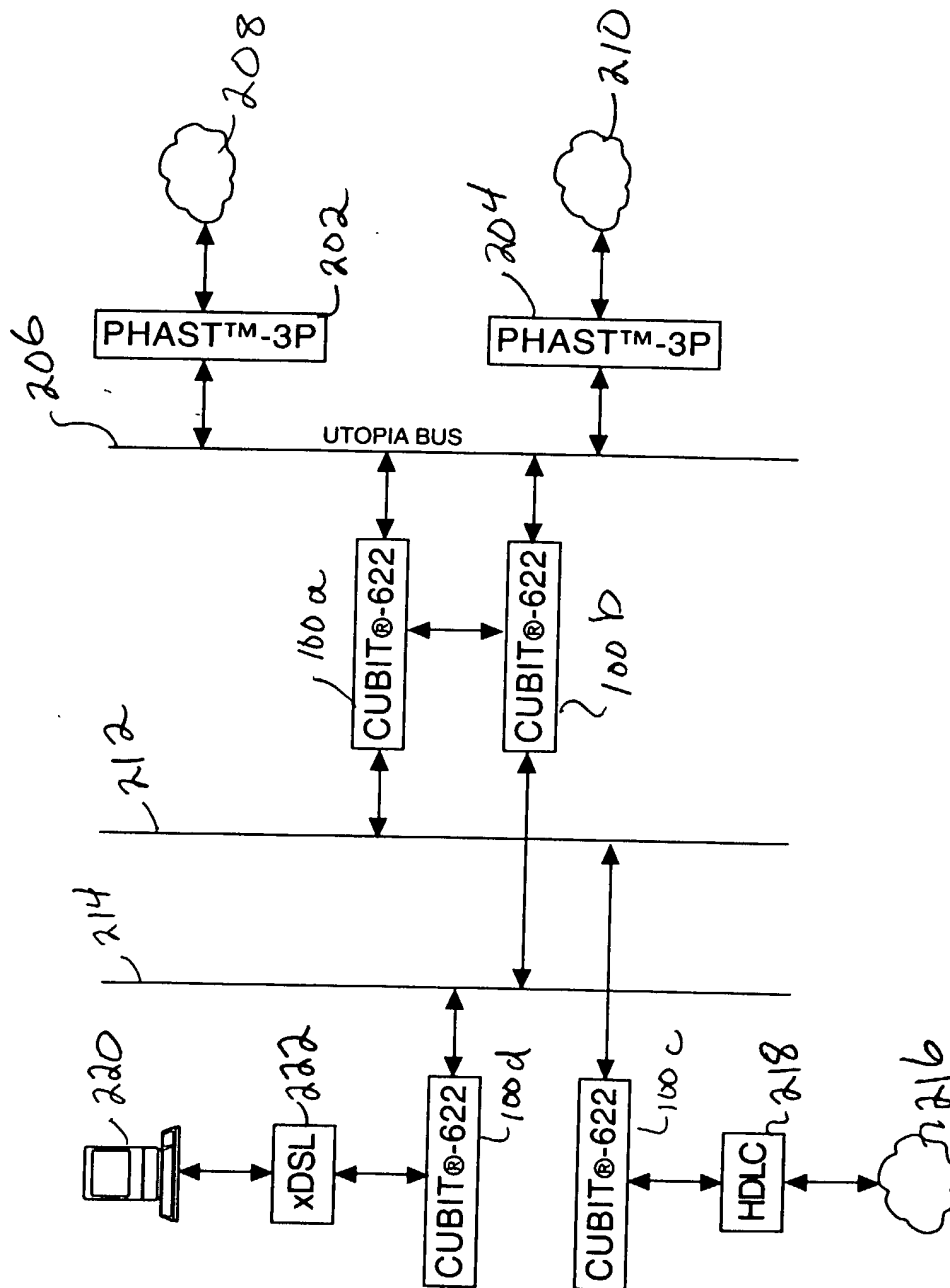


Figure 6